Cheaply writing a fast interpreter

Code at https://github.com/ndmitchell/interpret



@ndm_haskell

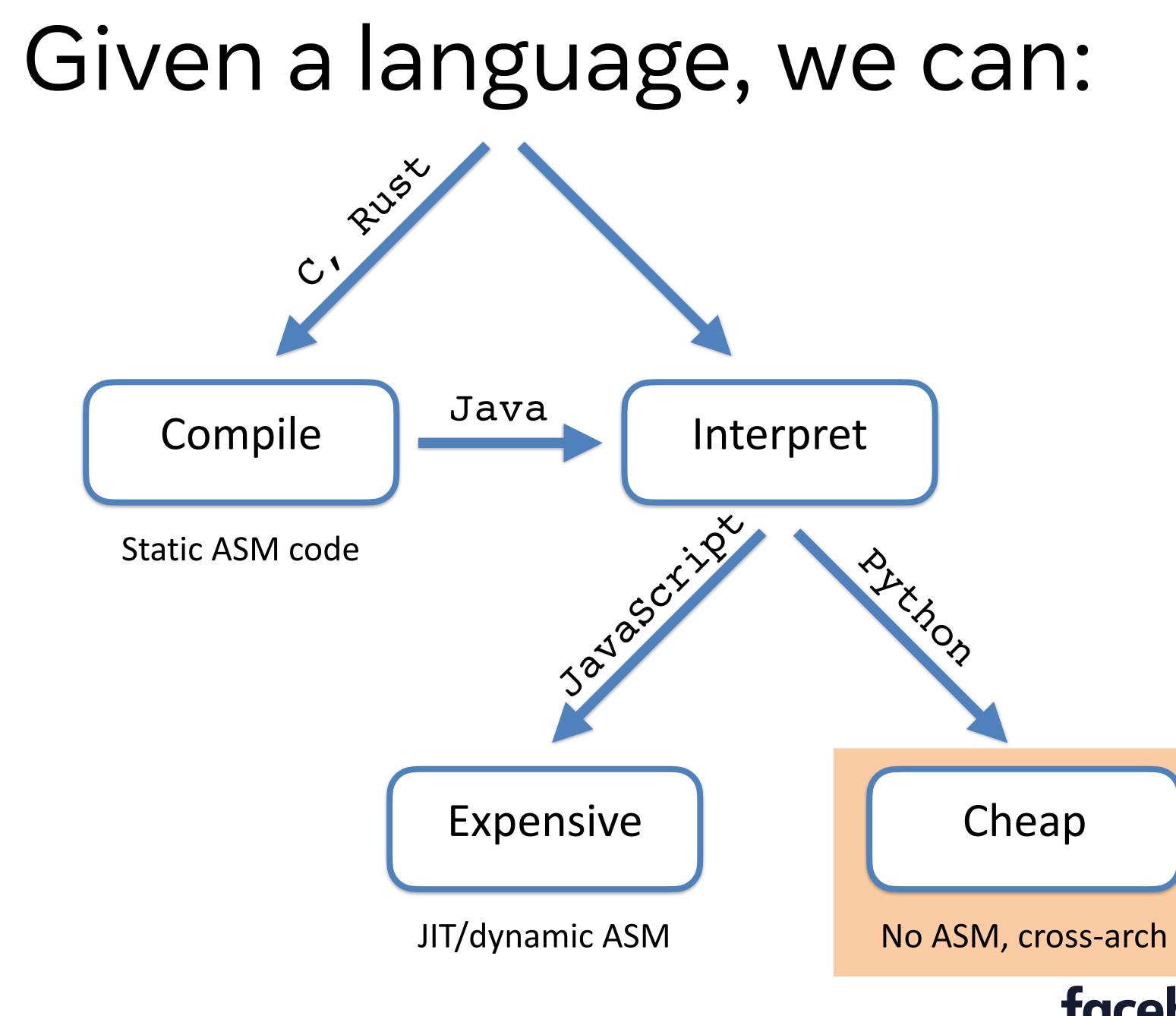


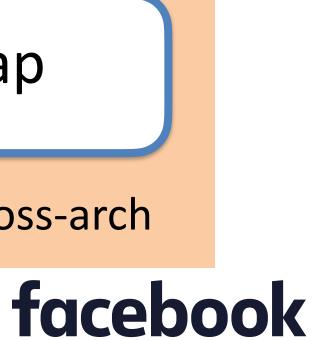


The options

Compile

Static ASM code

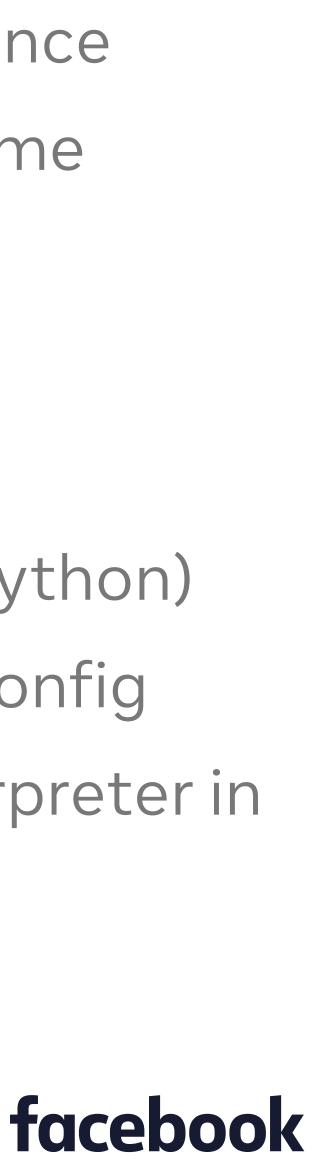




This talk

Cheap interpreters

- Low cost of development and maintenance No Assembly (ASM) writing (may be some
- reading)
- Can do better! But at cost (v8, Lua)
- An example: Starlark (aka deterministic Python) Used by Buck/Bazel build systems for config. How would we go about writing an interpreter in **Rust for Starlark?**



Approach

Possible alternatives

- AST (abstract syntax tree) interpretation
- Bytecode (threaded?)
- Closure generation
- Intermediates: Native, Stack, Registers?
- Packed/Unpacked?



Benchmarks



Example

x = 100; for (i = 1000; i != 0; i--) { x = x + 4 + x + 3; x = x + 2 + 4; } x

Deliberately use only +, to emphasise interpreter overhead In reality, an expensive atoms might make all this noise



WalkAST

 $\bullet \bullet \bullet$

fn f(x: &Expr, vs: V) -> i64 { match x { Lit(i) => *i,Var(u) => vs[u],Add(x, y) =>f(x, vs) + f(y, vs),Assign(u, e) => vs[u] = f(e, vs),



Guess

- Do the obvious things:
- Use unchecked array access
- Convert variables to indices
- No allocation
- Rust O
- (All these are always done in this talk)

What performance penalty?

What is the performance penalty?









1 day

2.5 minutes





Fairness

What did it do? x = x + 4 + x + 3;x = x + 2 + 4;x = x + x + 13;

Make add a noinline function call More representative of real work











1 minute





AST walk

- Match on AST nodes Perform operations
- Could we match on AST nodes only once? • Yes! Generate closures once, run closures Closure = function pointer + data

Rust

AST

What does it do?



Closures

AST Closure

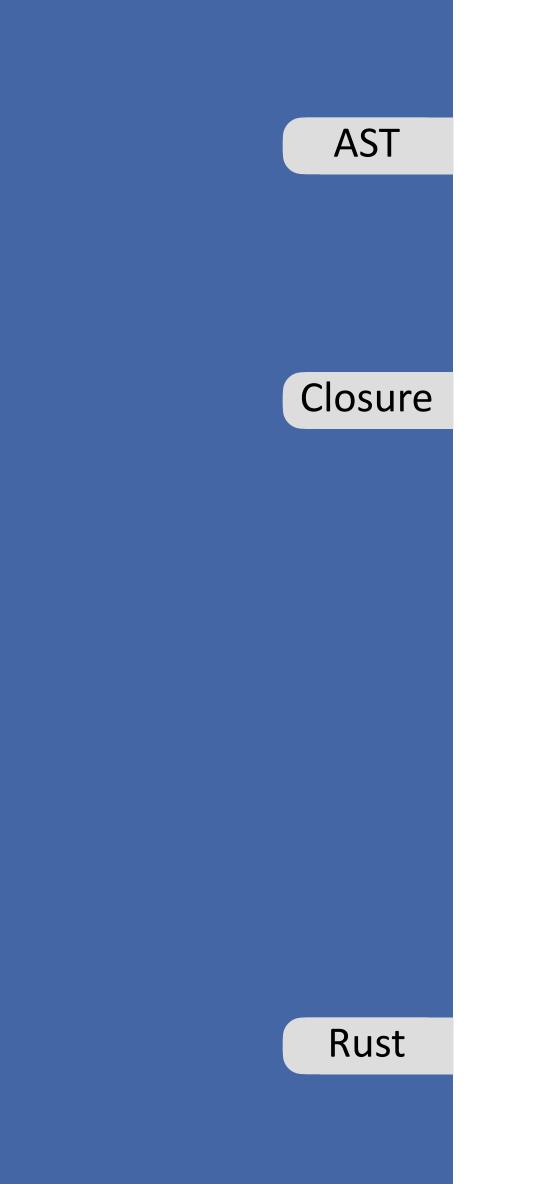
Rust

type $K = Box < dyn Fn(V) \rightarrow i64>;$

- fn f(x: $\& Expr) \rightarrow K \{$ match x { $Lit(i) => \{$ let i = *i;box move | i;
 - Add(x, y) => { let x = f(x);let y = f(y);box move |v| x(v) + y(v)



Storage



Where do intermediates go?

With AST/Closure we reuse the native/Rust stack • f(x, ...) + f(y, ...)

What could we do instead? Explicit:

Stack

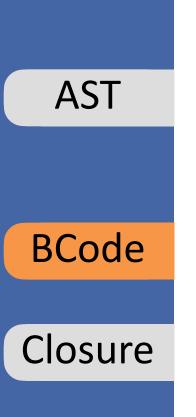
Access the top

- •PUSH1
- •ADD
- Poptop 2
- Push their sum

- Registers
 - Access by index
 - $\cdot r9 = 1$
 - r7 = r2 + r9



Bytecode



With a stack

PUSH GET ADD SET

loop {

 $\bullet \bullet \bullet$

Rust

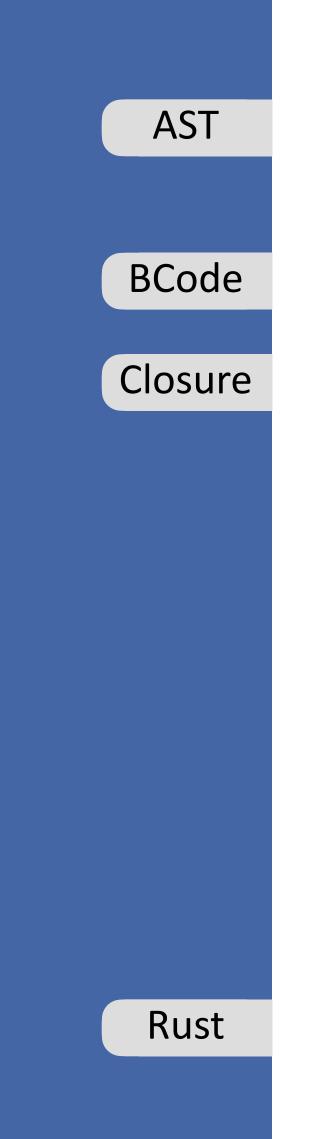
- ADD => stack.push(stack.pop() + stack.pop()),
- PUSH => stack.push(tape.next()),
- match tape.next() {
- Put variables at the bottom of the stack \$i
- -1 \$i







ASM view



loop { JUMP

 $\bullet \bullet \bullet$

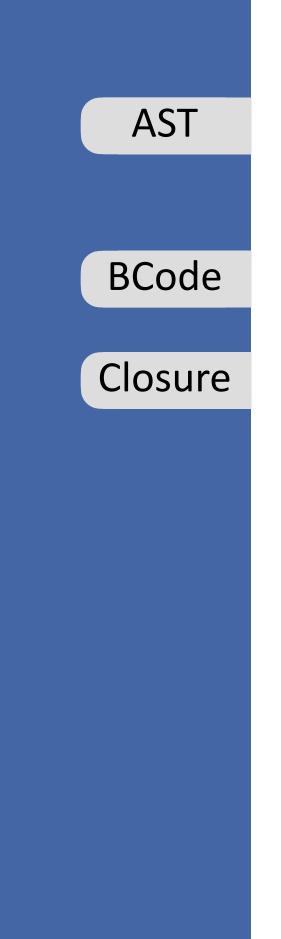
JUMP

What happens on each op?

- match tape.next() { LOOKUP match[tape.next()]
 - BODY
 - 'loop



ASM view



- Can't generate new ASM on the fly
- The definition of a "Cheap" interpreter
- Must have a finite number of parameterisable chunks of ASM

Rust

What would be optimal?

- Must JUMP between them but only one JUMP
- Sometimes known as "direct threading"





C + + (GCC)

AST BCode Closure Rust

push:

add:

set:

Computed goto

static const Tape tape = {&&push, 1, &&add, &&set, 8, ...};

- stack.push(tape.next()); goto tape.next();
- stack.push(stack.pop() + stack.pop()); goto tape.next();



Rust

Faking computed Goto

- Tail calls are compiled to JUMP
- On x86_64, with -O
- Not guaranteed (can abstract it)
- But is compositional 🙂
- fn add(stack: Stack, tape: Tape) { stack.push(

 - k(stack, tape);

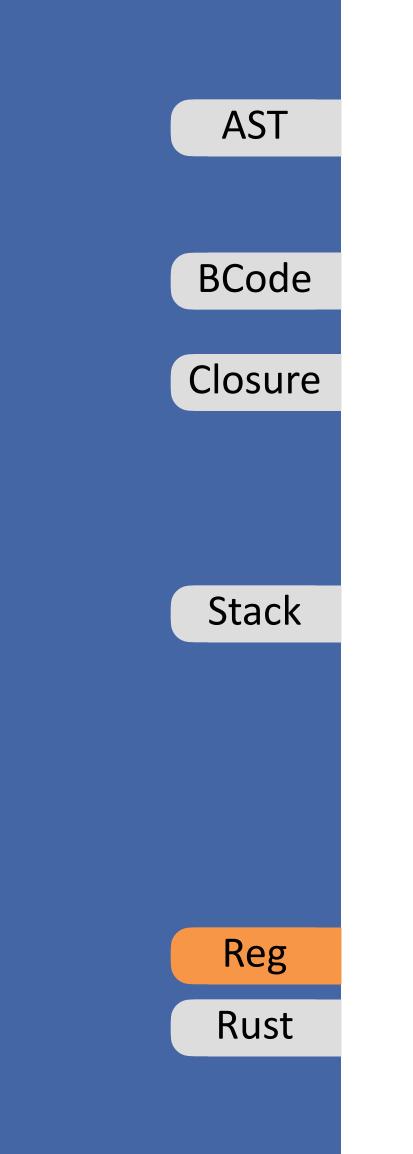
AST BCode Closure Stack Rust

- stack.pop() + stack.pop);
- let k = tape.next();





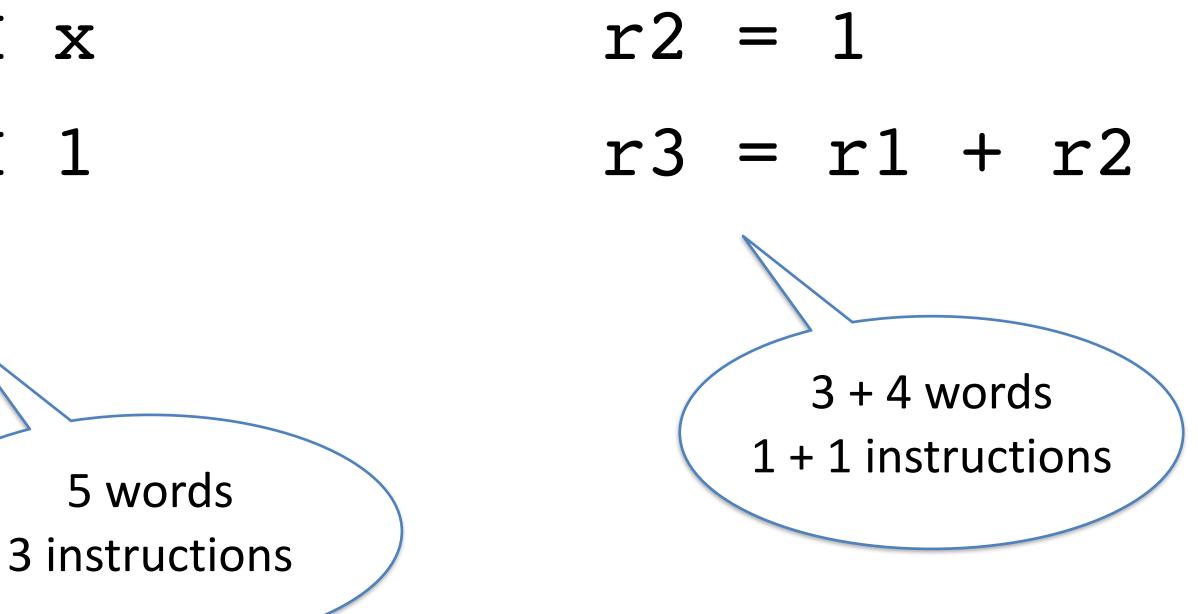
Evenfaster



Use registers

- Longer instructions, but fewer Less adjusting the stack

PUSH x PUSH 1 ADD





What else?



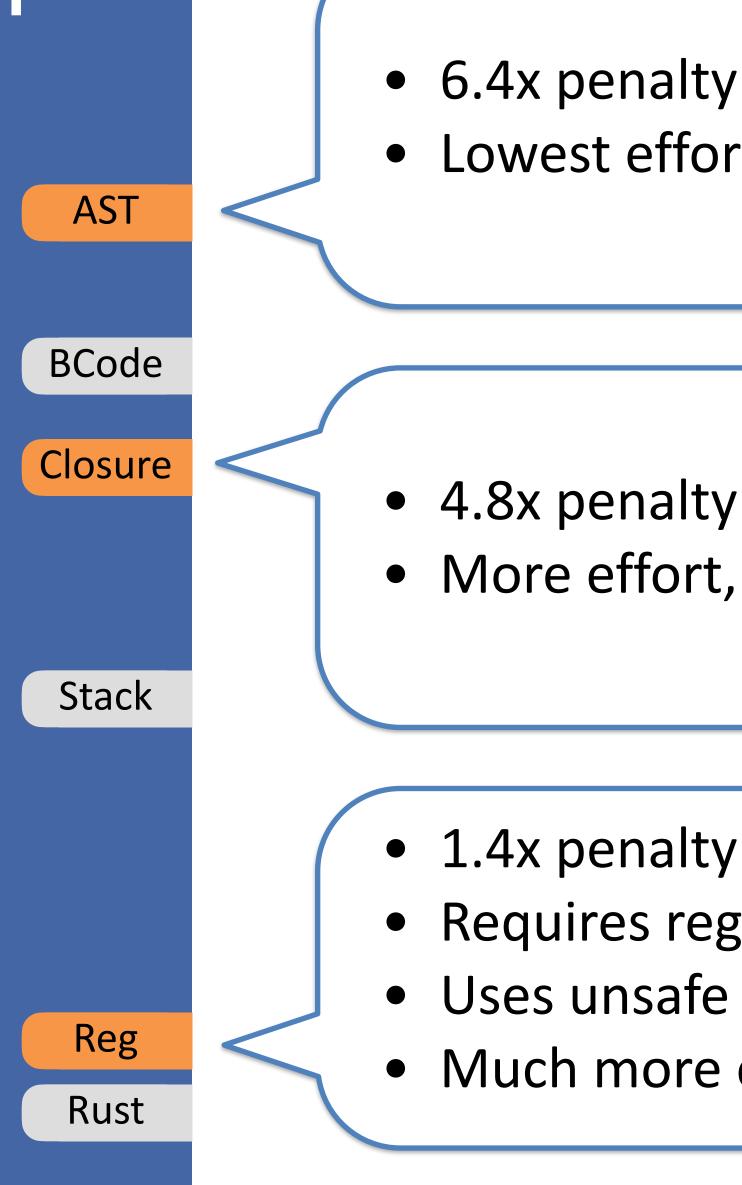
Didn't work

- Use compact tape instead of word-aligned
- A few percent slowdown
- A better register allocator (less registers)
- No difference on this particular benchmark
- - Transform the code first (e.g. 2 + 4 => 6) •Use "bigger" fragments (e.g. add3) Generate fresh assembly at runtime

Would work



Conclusion



• Lowest effort, cleanest code

• More effort, but not *much* more

1.4x penalty **Requires register allocator** Uses unsafe operations (register indexing) • Much more effort, but much better result

